

# LZ23132

1/3 type Color CCD Area Sensor for NTSC

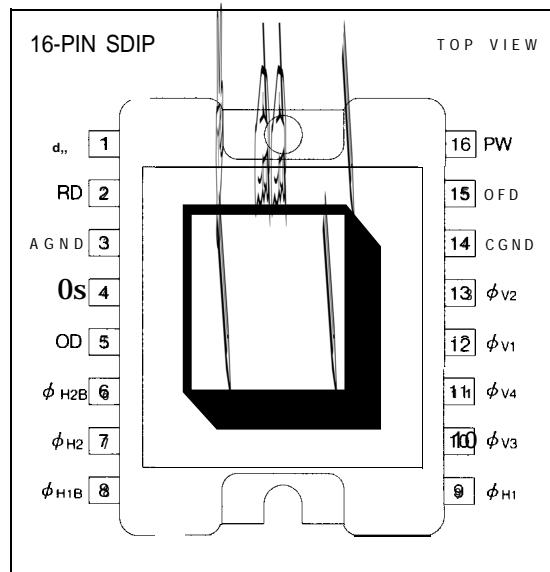
## DESCRIPTION

LZ23132 is a 1/3-type (6.0 mm) solid-state image sensor that consists of PN photo-diodes and CCDS (charge-coupled devices). Having approximately 270000 pixels (horizontal 542 × vertical 492), the sensor provides a high resolution stable color normal or mirror image.

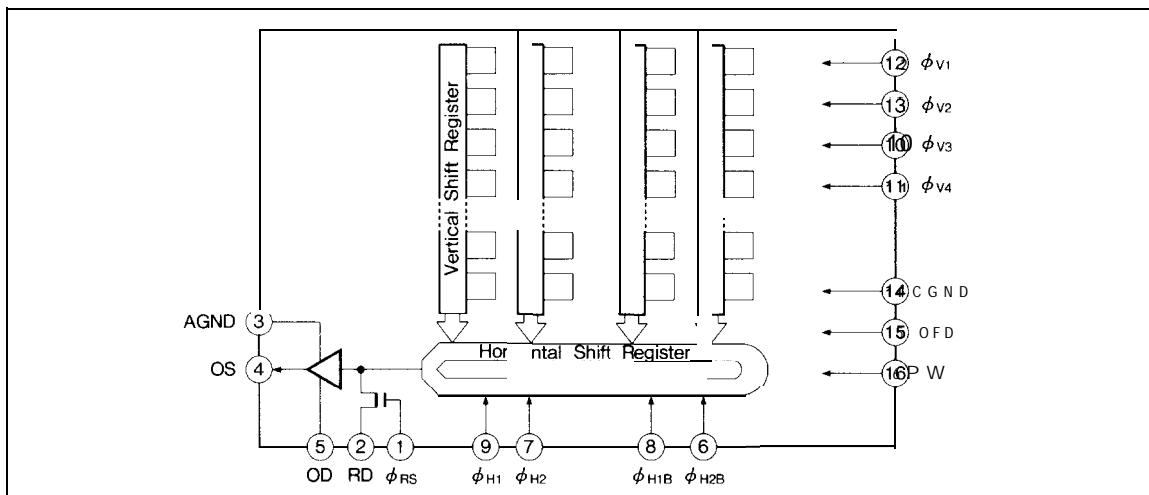
## FEATURES

- Number of pixels : 512 (H) × 492 (V)
- Pixel pitch : 9.6  $\mu\text{m}$  (H) × 7.5  $\mu\text{m}$  (V)
- Number of optical black pixels  
    : Horizontal; front 2 and rear 28
- Complementary color filters of Mg, G, Cy and Ye
- Low fixed pattern noise and lag
- No sticking and no image distortion
- Blooming suppression structure
- Built-in output amplifier
- Variable electronic shutter (1/60 to 1/1 0 000 s)
- Compatible with NTSC standard
- Normal or mirror image output available from common output terminal
- Package : 16-pin SDIPICERDIP](WDIPOI 6-N-0500B)

## PIN CONNECTIONS



## BLOCK DIAGRAM



## PIN DESCRIPTION

SYMBOL	PIN NAME	NOTE
RD	Reset transistor drain	
OD	Output transistor drain	
Os	Video output	
$\phi_{RS}$	Reset transistor gate clock	
$\phi_{V1}, \phi_{V2}, \phi_{V3}, \phi_{V4}$	Vertical shift register gate clink	
$\phi_{H1}, \phi_{H2}, \phi_{H1B}, \phi_{H2B}$	Horizontal shift register gate clock	1
OFD	Overflow drain	
PW	P type well	
AGND	Analog part ground	
CGND	Clock part ground	

## NOTE :

1. Normal image output mode :  $\phi_{H1} = \phi_{H1B}, \phi_{H2} = \phi_{H2B}$

Mirror image output mode :  $\phi_{H1} = \phi_{H2B}, \phi_{H2} = \phi_{H1B}$

CCD AREA N Ors



## ABSOLUTE MAXIMUM RATINGS

(Ta = 25°C)

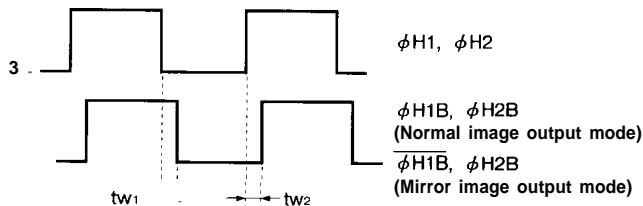
PARAMETER	SYMBOL	RATING	UNIT
Output transistor drain voltage	V <sub>OD</sub>	0 to +18	v
Reset transistor drain voltage	V <sub>RD</sub>	0 to +18	v
Overflow drain voltage	V <sub>Om</sub>	0 to +55	v
Reset gate clock voltage	V <sub><math>\phi_{RS}</math></sub>	-0.3 to +18	v
Vertical shift register clock voltage	V <sub><math>\phi_V</math></sub>	-9.0 to +18	v
Horizontal shift register clock voltage	V <sub><math>\phi_H</math></sub>	-0.3 to +18	v
Voltage difference between PW and vertical clock	V <sub>PW</sub> -V <sub><math>\phi_V</math></sub>	-27 to 0	v
Storage temperature	T <sub>stg</sub>	-40 to +85	°C
Operating ambient temperature	T <sub>opr</sub>	-20 to +70	°C

## RECOMMENDED OPERATING CONDITIONS

PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Operating ambient temperature		$T_{opr}$		25.0		'c	
Output transistor drain voltage		$V_{co}$	14.5	15.0	16.0	v	
Reset transistor drain voltage		$V_{RD}$		$V_{OD}$		v	
Overflow drain voltage	When DC is applied	$V_{om}$	5.0		19.0	v	1
	When pulse is applied p-p level	$V_{\phi OFD}$	22.0			v	2
Ground		AGND , CGND		0.0		v	
P-well voltage		$V_{PW}$	-9.0		$V_{\phi VL}$	v	
Vertical shift register clock	LOW level	$V_{\phi V1-4L}$	-8.5	-8.0	-7.5	v	
	INTERMEDIATE level	$V_{\phi V1-4I}$		0.0		v	
	HIGH level	$V_{\phi V1H}, V_{\phi V3H}$	16.0	16.5	17.0	v	
Horizontal shift register clock	LOW level	$V_{\phi H1-2L}, V_{\phi H1B-2BL}$	-0.05	0.0	0.05	v	
	HIGH level	$V_{\phi H1-2H}, V_{\phi H1B-2BH}$	4.7	5.0	6.0	v	
Reset gate clock	LOW level	$V_{\phi RSL}$	0.0		$V_{RD}-12.0$	v	
	HIGH level	$V_{\phi RSH}$			9.5	v	
Vertical shift register clock frequency		$f_{\phi V1-4}$		15.73		kHz	
Horizontal shift register clock frequency		$f_{\phi H1-2}, f_{\phi H1B-2B}$		9.53		MHz	
Reset gate clock frequency		$f_{\phi RS}$		9.53		MHz	
Horizontal shift register clink phase		$tw_1, tw_2$	0.0	5.0	10.0	ns	3

## NOTES :

1. When DC voltage is applied, shutter speed is 1 /60 seconds
2. When pulse is applied, shutter speed is less than 1/60 seconds.



**ELECTRICAL CHARACTERISTIC (Drive method : Field Accumulation)**

(Ta = 25°C, Operating conditions : typical values for the recommended operating conditions, Color temperature of light source : 3200 K / IR cut-off filter (CM-500, 1 mmt))

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Photo response non-uniformity	PRNU			10	%	2
Carrier saturation	Vsat	400	450		mV	3
Dark output voltage	Vdark		0.3	3.0	mV	1, 4
Dark signal non-uniformity	DSNU		0.6	2.0	mV	1, 5
Sensitivity	R	260	330		mV	6
Gamma	Y		1			
Smear ratio	SMR		-81	-76	dB	7
Image lag	AI			1.0	%	8
Blooming suppression ratio	ABL	1000				9
Output transistor drain current	Iod		4.0	8.0	mA	
Output impedance	Ro		300		Ω	
Dark noise	Vnoise		0.2	0.3	mV	10
OB difference in level				1.0	mV	11
Vector breakup				5.0	,/0	12
Line crawling				3.0	%	13
Luminance flicker				2.0	%	14

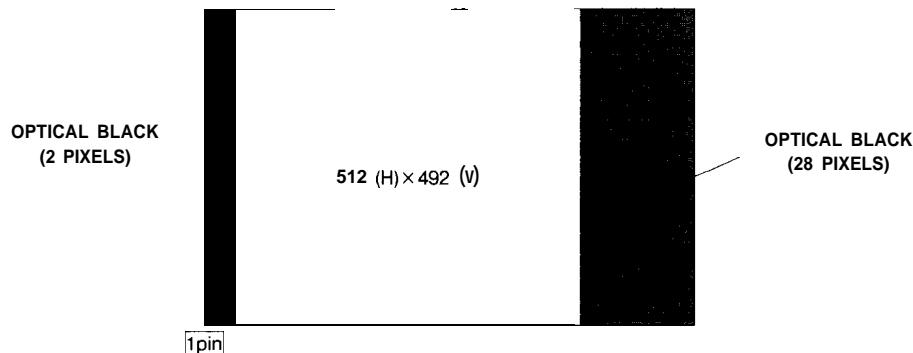
- The standard output voltage is defined as 150 mV by the average output voltage under uniform illumination.
- The standard exposure level is defined when the average output voltage is 150 mV under uniform illumination.
- Vom should be adjusted to the minimum voltage with that ABL satisfy the specification.**

## NOTES :

- Ta : +60°C
- The image area is divided into 10X 10 segments. The segment's voltage is the average output voltage of all the pixels within the segment. PRNU is defined by  $(V_{max} - V_{min})/V_o$ , where  $V_{max}$  and  $V_{min}$  are the maximum and the minimum values of each segment's voltage respectively, when the average output voltage  $V_o$  is 150 mV.
- The output voltage measured at the carrier peak when carrier signal reaches maximum.
- The average output voltage under a non-exposure condition.
- The image area is divided into 10x 10 segments. DSNU is defined by  $(V_{dmax} - V_{dmin})$  under the non-exposure condition where  $V_{dmax}$  and  $V_{dmin}$  are the maximum and the minimum values of each segment's voltage, respectively, that is the average output voltage over all pixels in the segment.
- The average output voltage when a 1000 lux light source attached with a 900f. reflector is imaged by a lens of F4, f50 mm.

- The sensor is adjusted to position a V/I O square at the center of image area where V is the vertical length of the image area. SMR is defined by the ratio of the output voltage detected during the vertical blanking period to the maximum of the pixel voltage in the V/I O square.
- The sensor is exposed at the exposure level corresponding to the standard condition preceding non-exposure condition. AI is defined by the ratio between the output voltage measured at the 1st field during the non-exposure period and the standard output voltage.
- The sensor is adjusted to position a V/I O square at the center of image area. ABL is the ratio between the exposure at the standard condition and the exposure at a point where a blooming is observed.
- The RMS value of the dark noise (after CDS). The bandwidth range is from 1.00 kHz to 4.2 MHz. SC trap on.
- The difference between the average output voltage of the effective area and the OB part under the non-exposure condition.
- Observed with a vector scope when the color bar chart is imaged under the standard exposure condition.
- The difference between the average output voltage of the (Mg + Ye), (G+ Cy) line and the (Mg + Cy), (G+ Ye) line under the standard exposure condition.
- The difference between the average output voltage of the odd field and the even field.

## PIXEL STRUCTURE



## COLOR FILTER ARRAY

(1,492)

Ye	Cy	Ye	Cy	Ye
<b>G</b>	<b>Mg</b>	<b>G</b>	<b>Mg</b>	<b>GG</b>
Ye	Cy	Ye	Cy	Ye
<b>Mg</b>	<b>G</b>	<b>Mg</b>	<b>G</b>	<b>Mg</b>
Ye	Cy	Ye	Cy	Ye
<b>G</b>	<b>Mg</b>	<b>G</b>	<b>Mg</b>	<b>GG</b>

(512,492)

Cy	Ye	Cy	Ye	Cy
<b>Mg</b>	<b>G</b>	<b>Mg</b>	<b>GG</b>	<b>Mg</b>
Cy	Ye	Cy	Ye	Cy
<b>G</b>	<b>Mg</b>	<b>G</b>	<b>Mg</b>	<b>GG</b>
Cy	Ye	Cy	Ye	Cy
<b>Mg</b>	<b>G</b>	<b>Mg</b>	<b>G</b>	<b>Mg</b>

Ye	Cy	Ye	Cy	Ye
<b>Mg</b>	<b>G</b>	<b>Mg</b>	<b>G</b>	<b>MgMg</b>
Ye	Cy	Ye	Cy	Ye
<b>G</b>	<b>Mg</b>	<b>G</b>	<b>Mg</b>	<b>GG</b>
Ye	Cy	Ye	Cy	Ye
<b>Mg</b>	<b>GG</b>	<b>Mg</b>	<b>G</b>	<b>MgMg</b>

(1,1)

ODD field

Cy	Ye	Cy	Ye	Cy
<b>G</b>	<b>Mg</b>	<b>G</b>	<b>Mg</b>	<b>G</b>
Cy	Ye	Cy	Ye	Cy
<b>Mg</b>	<b>G</b>	<b>Mg</b>	<b>G</b>	<b>Mg</b>
Cy	Ye	Cy	Ye	Cy
<b>G</b>	<b>Mg</b>	<b>G</b>	<b>Mg</b>	<b>G</b>

(512,1)

EVEN field

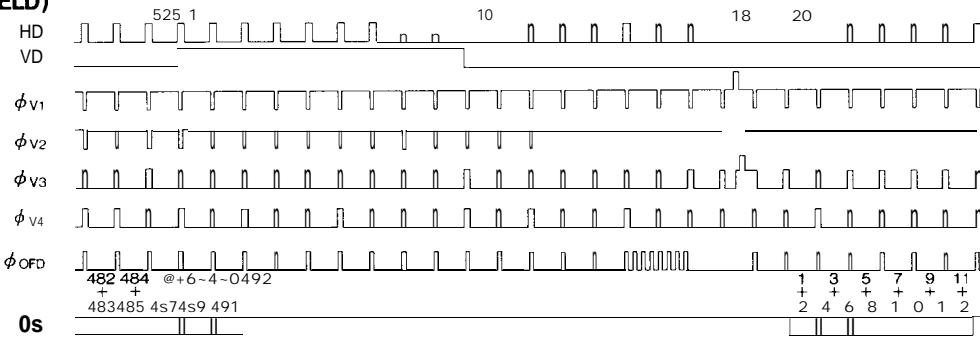
## TIMING DIAGRAM EXAMPLE

## VERTICAL TRANSFER TIMING &lt; NORMAL OUTPUT &gt;

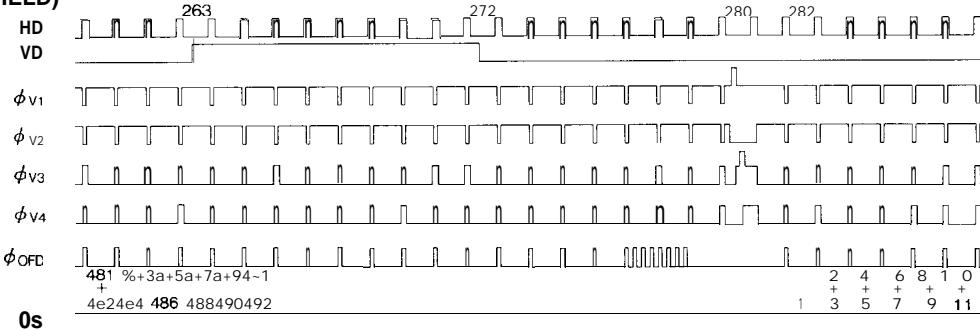
Shutter speed

1/10000 s

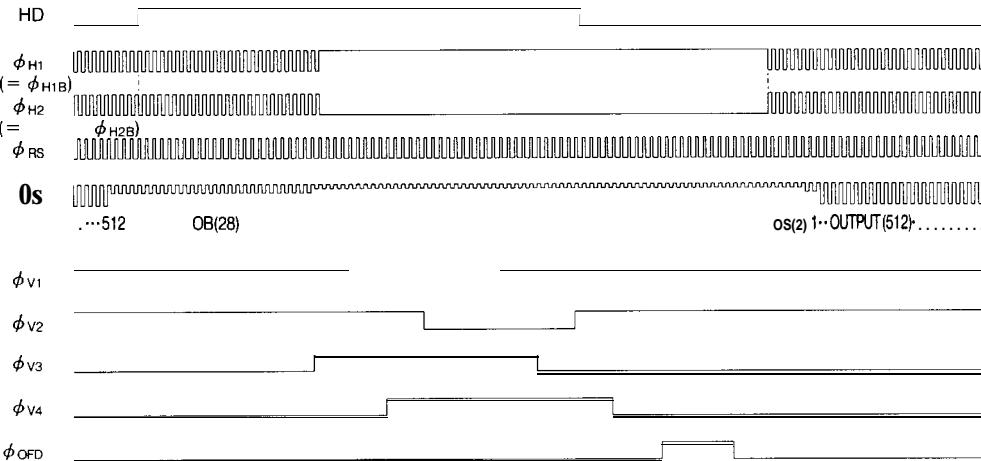
## (ODD FIELD)



## (EVEN FIELD)



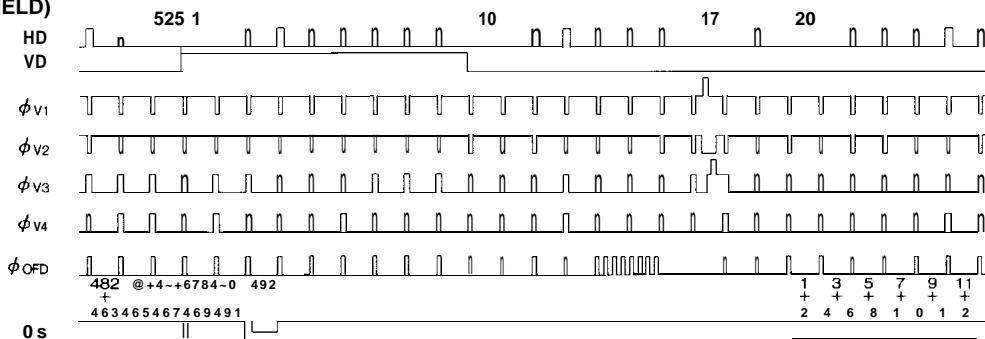
## HORIZONTAL TRANSFER TIMING &lt; NORMAL OUTPUT &gt;



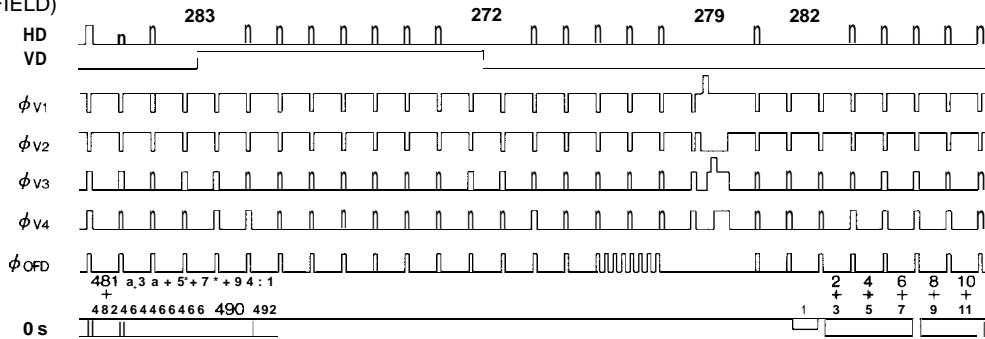
## VERTICAL TRANSFER TIMING &lt; MIRROR OUTPUT &gt;

Shutter speed  
1/loop s

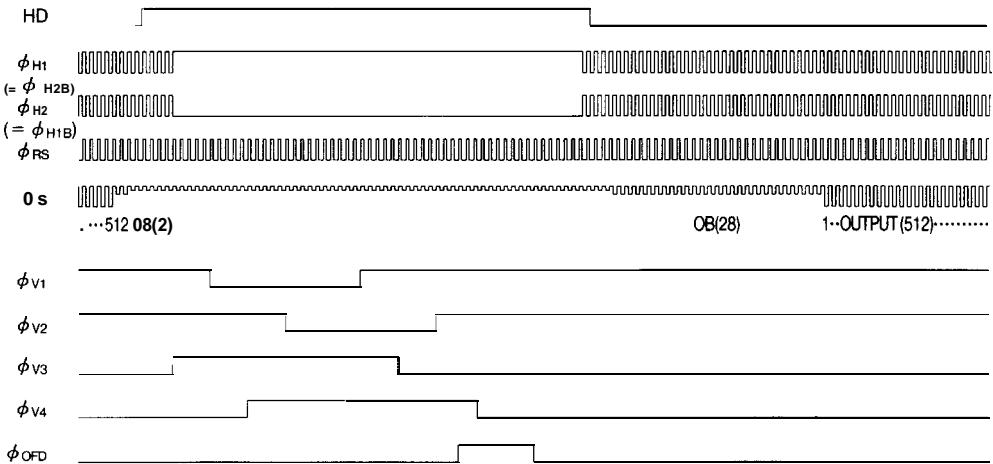
## (ODD FIELD)



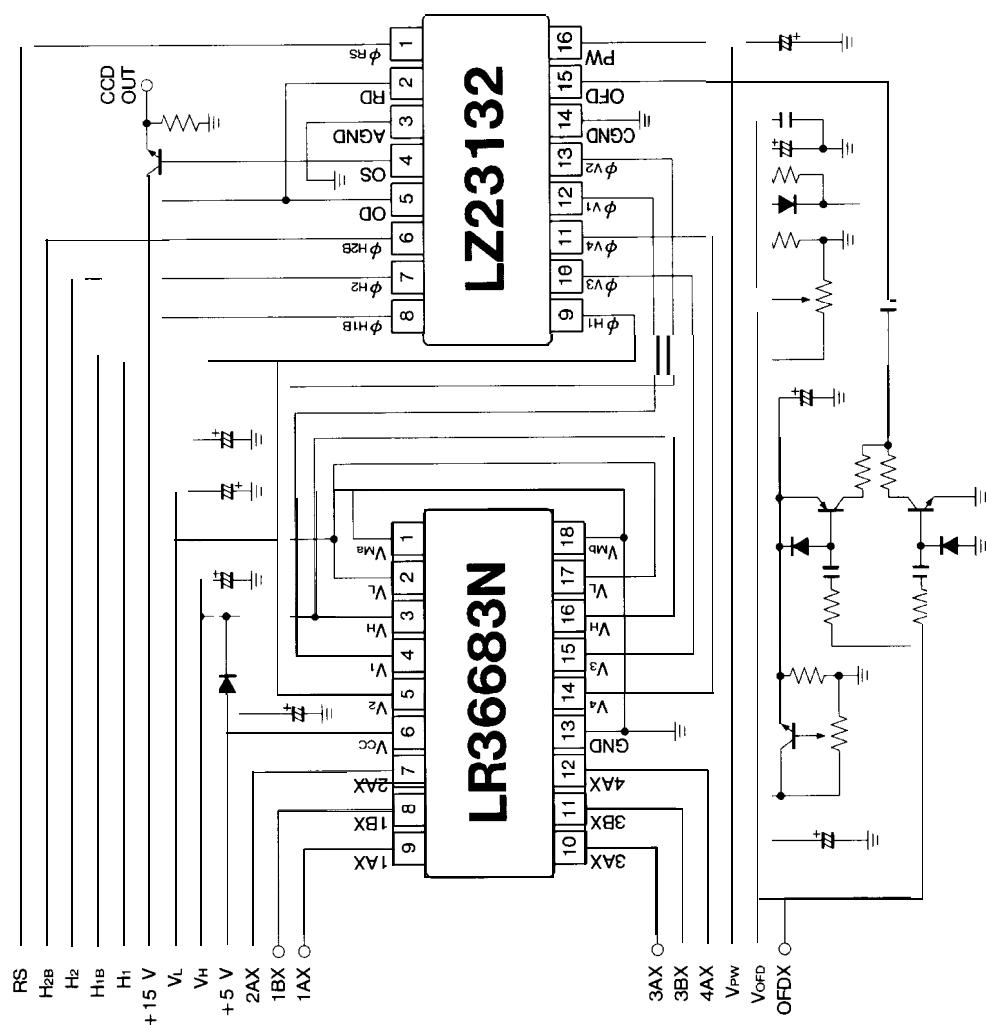
## (EVEN FIELD)



## HORIZONTAL TRANSFER TIMING &lt; MIRROR OUTPUT &gt;



## SYSTEM CONFIGURATION EXAMPLE



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